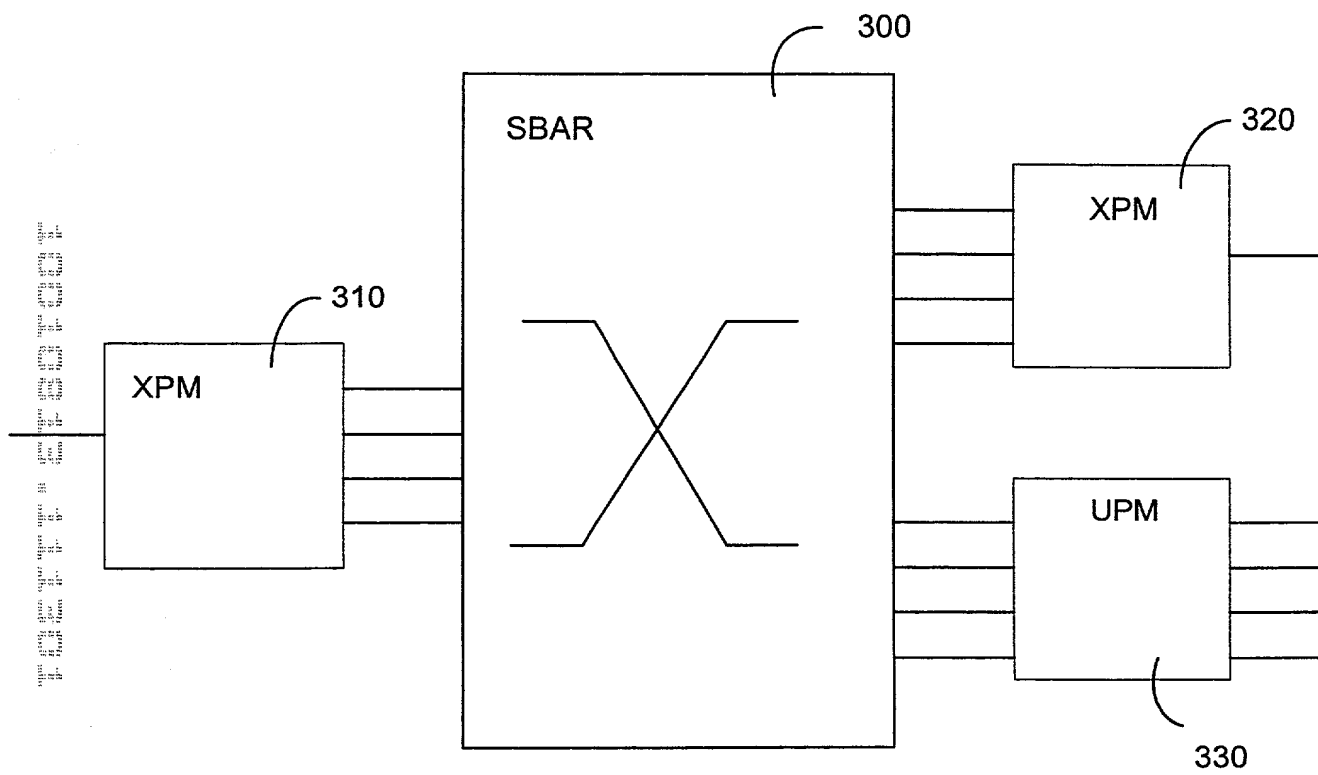


**FIGURE 1**  
**(prior art)**





**FIGURE 3**



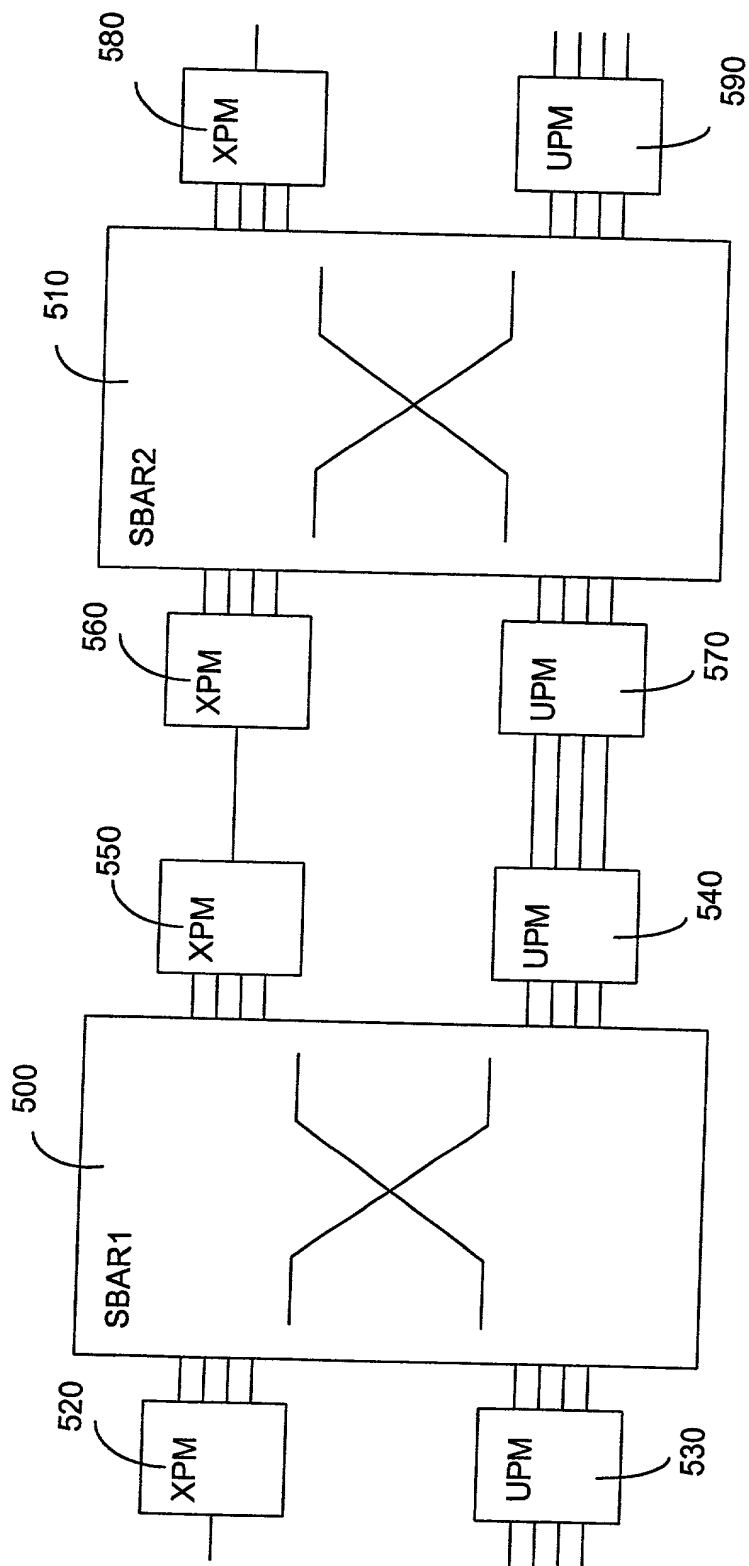


FIGURE 5

Figure 6 is a schematic diagram of a system 200. The system 200 includes a main processing unit 205 and a peripheral device 230. The main processing unit 205 is connected to the peripheral device 230 via a communication link 270. The peripheral device 230 includes a display 235 and a set of input devices 610, 620, 630, 640, and 650. The input devices 610, 620, 630, 640, and 650 are arranged in a row and are connected to the peripheral device 230 via a common bus 600. The main processing unit 205 is labeled with the text "XPM".

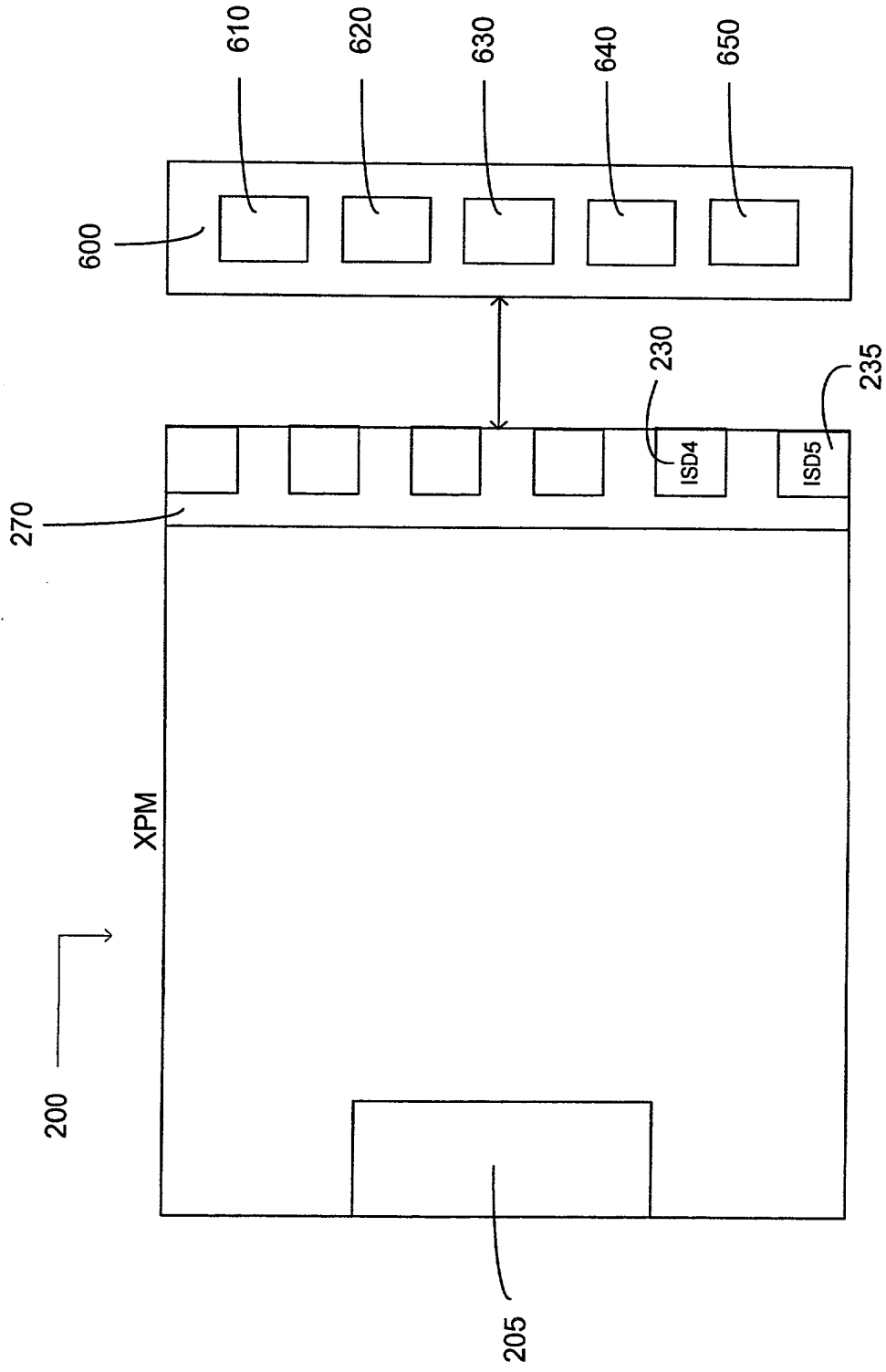


FIGURE 6